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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Suaris et al.

Application No. 10/785,608

Filed: February 23, 2004

Confirmation No. 3195

For: MEMORY RE- IMPLEMENTATION FOR  
FIELD PROGRAMMABLE GATE  
ARRAYS

Examiner: James M. Mitchell

Art Unit: 2813

Attorney Reference No. 1011-67730-01

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PURSUANT TO 37 C.F.R. § 1.97(b)

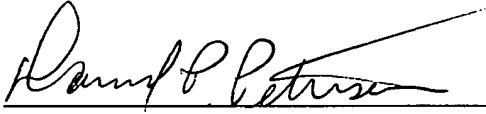
Listed on the accompanying form PTO-1449 and enclosed herewith is an English-language document. Applicants respectfully request that the document be listed as a reference cited on the issued patent.

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Respectfully submitted,

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**INFORMATION DISCLOSURE STATEMENT  
BY APPLICANT**

Attorney Docket Number	1011-67730-01
Application Number	10/785,608
Filing Date	February 23, 2004
First Named Inventor	Suaris
Art Unit	2813
Examiner Name	James M. Mitchell

**U.S. PATENT DOCUMENTS**

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Patentee

**FOREIGN PATENT DOCUMENTS**

Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee

**OTHER DOCUMENTS**

Examiner's Initials*	Cite No. (optional)	
		Suaris et al., "Incremental Physical Resynthesis for Timing Optimization", February 22, 2004

**EXAMINER  
SIGNATURE:**
**DATE  
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\* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.